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Inclosure Configuration:

Dual-in-line

Output Logic Form:

Transistor-transistor logic

Input Circuit Pattern:

Quad 2 input

Criticality Code Justification:

Feat

Design Function And Quantity:

4 gate, nand

Case Outline Source And Designator:

D-1 mil-m-38510

Special Features:

Electrostatic discharge sensitive

Shelf Life:

N/a

Unit Of Measure:

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Demilitarization:

No

Fiig:

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