# NSN 5962-01-247-9573

Memory Microcircuit - Page 1 of 1



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# **Body Length:**

Between 0.880 inches and 0.900 inches

# Body Width:

Between 0.285 inches and 0.300 inches

# **Body Height:**

Between 0.145 inches and 0.170 inches

## **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

## Storage Tempurature Range:

-65.0/+150.0 degrees celsius

## **Features Provided:**

Monolithic and programmable and schottky and high speed and 3-state output

## Inclosure Material:

Ceramic

#### Inclosure Configuration:

Dual-in-line

#### **Output Logic Form:**

Transistor-transistor logic

## Input Circuit Pattern:

12 input

**Terminal Surface Treatment:** 

Solder

## Voltage Rating And Type Per Characteristic:

-0.5 volts power source and 7.0 volts power source

## Time Rating Per Chacteristic:

70.00 nanoseconds propagation delay time, low to high level output and 70.00 nanoseconds propagation delay time, high to low level output

#### Memory Device Type:

Rom

## Memory Capacity:

Unknown

## Test Data Document:

96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).

## Terminal Type And Quantity:

18 printed circuit

#### Shelf Life:

N/a

## Unit Of Measure:

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#### Demilitarization:

Yes - demil/mli

#### Fiig:

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