## NSN 5962-01-257-5984

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## **Body Length:** 0.540 inches **Body Width:** Between 0.245 inches and 0.300 inches **Body Height:** Between 0.045 inches and 0.100 inches **Operating Tempurature Range:** -55.0/+150.0 degrees celsius Storage Tempurature Range: -65.0/+150.0 degrees celsius **Features Provided:** Monolithic and programmable and bipolar **Inclosure Material:** Ceramic **Inclosure Configuration:** Flat pack **Output Logic Form:** Transistor-transistor logic **Input Circuit Pattern:** 16 input **Case Outline Source And Designator:** F-9 mil-m-38510 **Current Rating Per Characteristic:** 100.00 milliamperes reverse current, dc **Terminal Surface Treatment:** Solder **Product Name:** And-or gate array **Voltage Rating And Type Per Characteristic:** -0.5 volts power source and 7.0 volts power source Memory Device Type: Pal **Test Data Document:** 96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.). **Terminal Type And Quantity:** 20 flat leads Shelf Life: N/a Unit Of Measure: ---**Demilitarization:** Yes - demil/mli

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