NSN 5962-01-263-0962

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Body Length:
Between 0.342 inches and 0.358 inches
Body Width:
Between 0.342 inches and 0.358 inches
Body Height:
Between 0.064 inches and 0.100 inches
Maximum Power Dissipation Rating:
104.5 milliwatts
Operating Tempurature Range:
-55.0/+125.0 degrees celsius
Storage Tempurature Range:
-65.0/+150.0 degrees celsius
Features Provided:
Low power and schottky and w/clear and w/clock
Inclosure Material:
Ceramic
Inclosure Configuration:
Leadless flat pack
Output Logic Form:
Transistor-transistor logic
Input Circuit Pattern:
8 input
Design Function And Quantity:
6 flip-flop, d-type
Case Outline Source And Designator:
C-2 mil-m-38510
Terminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
-0.5 volts power source and 7.0 volts power source
Time Rating Per Chacteristic:
20.00 nanoseconds propagation delay time, low to high level output and 24.00 nanoseconds propagation delay time, high to low level
output
Test Data Document:
96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).
Terminal Type And Quantity:
20 leadless
Shelf Life:
N/a
Unit Of Measure:

Demilitarization: Yes - demil/mli

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