NSN 5962-01-266-8058

Memory Microcircuit - Page 1 of 1



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Body Length:
Between 1.440 inches and 1.500 inches
Body Width:
Between 0.510 inches and 0.550 inches
Body Height:
Between 0.145 inches and 0.165 inches
Maximum Power Dissipation Rating:
525.0 milliwatts
Operating Tempurature Range:
-0.0/+70.0 degrees celsius
Storage Tempurature Range:
-65.0/+125.0 degrees celsius
Features Provided:
Electrostatic sensitive and ultraviolet erasable and w/enable and programmable
Inclosure Material:
Ceramic
Inclosure Configuration:
Dual-in-line
Output Logic Form:
Metal oxide-semiconductor logic
Input Circuit Pattern:
17 input
Terminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
13.5 volts power source
Time Rating Per Chacteristic:
250.00 nanoseconds propagation delay time, low to high level output and 250.00 nanoseconds propagation delay time, high to low level
output
Memory Device Type:
Rom
Test Data Document:
96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).
Terminal Type And Quantity:
28 printed circuit
Shelf Life:
N/a
Unit Of Measure:
Demilitarization:
Yes - demil/mli

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