NSN 5962-01-269-0990

Memory Microcircuit - Page 1 of 1



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Inclosure Configuration:
Dual-in-line
Output Logic Form:
Metal oxide-semiconductor logic
Input Circuit Pattern:
17 input
Terminal Surface Treatment:
Solder
Memory Device Type:
Rom
Terminal Type And Quantity:
28 printed circuit
Shelf Life:
N/a
Unit Of Measure:
Demilitarization:
Yes - demil/mli
Fiig:
A458a0