NSN 5962-01-274-1597

Memory Microcircuit - Page 1 of 1

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Body Length:
1.405 inches
Body Width:
0.605 inches
Body Height:
0.200 inches
Maximum Power Dissipation Rating:
2.0 watts
Operating Tempurature Range:
-55.0/+125.0 degrees celsius
Storage Tempurature Range:
-65.0/+150.0 degrees celsius
Features Provided:
Hermetically sealed and burn in and programmable and bipolar and 3-state output
Inclosure Material:
Ceramic
Inclosure Configuration:
Dual-in-line
Output Logic Form:
Transistor-transistor logic
Input Circuit Pattern:
17 input
Terminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
7.0 volts power source
Time Rating Per Chacteristic:
80.00 nanoseconds propagation delay time, low to high level output and 80.00 nanoseconds propagation delay time, high to low lev
output
Memory Device Type:
Pal
Test Data Document:
96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).
Terminal Type And Quantity:
28 printed circuit
Shelf Life:
N/a
Unit Of Measure:
Demilitarization:
Yes - demil/mli
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