NSN 5962-01-274-7050

Memory Microcircuit - Page 1 of 1



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Features Provided:

Programmable and erasable and positive outputs

Output Logic Form:

Complementary-metal oxide-semiconductor logic

Input Circuit Pattern:

14 input

Criticality Code Justification:

Feat

Terminal Surface Treatment:

Solder

Memory Device Type:

Rom

Special Features:

Item must comply with requirements of defense electronics supply center production standard no. L03964; for navy nuclear propulsion plant

products only

Shelf Life:

N/a

Unit Of Measure:

Demilitarization:

Yes - demil/mli

Fiig:

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