NSN 5962-01-277-7138

Memory Microcircuit - Page 1 of 1



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Body Length:

Between 0.540 inches and 0.560 inches

Body Width:

Between 0.442 inches and 0.458 inches

Body Height:

Between 0.060 inches and 0.120 inches

Operating Tempurature Range:

-55.0/+125.0 degrees celsius

Storage Tempurature Range:

-65.0/+150.0 degrees celsius

Features Provided:

Hermetically sealed and programmable and schottky and 3-state output and positive outputs

Inclosure Material:

Ceramic

Inclosure Configuration:

Leadless flat pack

Output Logic Form:

Transistor-transistor logic

Input Circuit Pattern:

14 input

Terminal Surface Treatment:

Solder

Voltage Rating And Type Per Characteristic:

5.0 volts power source

Time Rating Per Chacteristic:

35.00 nanoseconds propagation delay time, low to high level output and 35.00 nanoseconds propagation delay time, high to low level output

Memory Device Type:

Rom

Terminal Type And Quantity:

32 leadless

Shelf Life:

N/a

Unit Of Measure:

Demilitarization:

Yes - demil/mli

Fiig:

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