## NSN 5962-01-277-8710

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Body Length:
Between 0.540 inches and 0.560 inches
Body Width:
Between 0.442 inches and 0.458 inches
Body Height:
Between 0.060 inches and 0.120 inches
Operating Tempurature Range:
-55.0/+125.0 degrees celsius
Storage Tempurature Range:
-65.0/+150.0 degrees celsius
Features Provided:
Schottky and high performance and w/enable and w/clear and w/preset
Inclosure Material:
Ceramic
Inclosure Configuration:
Leadless flat pack
Output Logic Form:
Transistor-transistor logic
Input Circuit Pattern:
14 input
Terminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
7.0 volts power source
Time Rating Per Chacteristic:
20.00 nanoseconds propagation delay time, low to high level output and 25.00 nanoseconds propagation delay time, high to low level
output
Memory Device Type:
Rom
Test Data Document:
96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).
Terminal Type And Quantity:
32 leadless
Shelf Life:
N/a
Unit Of Measure:
<del></del>
Demilitarization:
Yes - demil/mli
Fiig:
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