# NSN 5962-01-281-6914

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# **Body Length:**

Between 0.750 inches and 0.785 inches

# **Body Width:**

Between 0.245 inches and 0.271 inches

# **Body Height:**

0.185 inches

## **Operating Tempurature Range:**

+0.0/+70.0 degrees celsius

## Storage Tempurature Range:

-65.0/+150.0 degrees celsius

## **Features Provided:**

Hermetically sealed and burn in and electrostatic sensitive and low power and schottky

## Inclosure Material:

Ceramic

#### Inclosure Configuration:

Dual-in-line

#### **Output Logic Form:**

Transistor-transistor logic

#### Input Circuit Pattern:

Dual 4 input

# **Design Function And Quantity:**

2 gate, nand

## **Terminal Surface Treatment:**

Solder

## Voltage Rating And Type Per Characteristic:

7.0 volts power source

## **Time Rating Per Chacteristic:**

15.00 nanoseconds propagation delay time, low to high level output and 15.00 nanoseconds propagation delay time, high to low level output

## **Test Data Document:**

96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).

## Terminal Type And Quantity:

14 printed circuit

#### Shelf Life:

N/a

# Unit Of Measure:

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## Demilitarization:

Yes - demil/mli

## Fiig:

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