NSN 5962-01-282-0588

Digital Microcircuit - Page 1 of 1



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Body Length:
Between 0.371 inches and 0.400 inches
Body Width:
Between 0.247 inches and 0.285 inches
Body Height:
Between 0.050 inches and 0.080 inches
Operating Tempurature Range:
-55.0/+125.0 degrees celsius
Storage Tempurature Range:
-65.0/+150.0 degrees celsius
Features Provided:
Hermetically sealed and burn in and w/clear and w/clock
Inclosure Material:
Ceramic
Inclosure Configuration:
Flat pack
Output Logic Form:
Transistor-transistor logic
Input Circuit Pattern:
8 input
Design Function And Quantity:
6 flip-flop, d-type
Case Outline Source And Designator:
Mo-001aa joint electron device engineering council
Terminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
7.0 volts power source
Time Rating Per Chacteristic:
30.00 nanoseconds propagation delay time, low to high level output and 35.00 nanoseconds propagation delay time, high to low level
output
Test Data Document:
96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).
Terminal Type And Quantity:
16 flat leads
Shelf Life:
N/a
Unit Of Measure:
Demilitarization:

Fiig:

A458a0

Yes - demil/mli