NSN 5962-01-282-5431

Memory Microcircuit - Page 1 of 1



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Features Provided:

Electrostatic sensitive and programmed

Inclosure Material:

Ceramic

Inclosure Configuration:

Dual-in-line

Output Logic Form:

Transistor-transistor logic

Terminal Surface Treatment:

Solder

Memory Device Type:

Rom

Terminal Type And Quantity:

16 printed circuit

Shelf Life:

N/a

Unit Of Measure:

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Demilitarization:

Yes - demil/mli

Fiig:

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