## NSN 5962-01-286-0730

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## **Body Length:**

Between 0.342 inches and 0.358 inches

## Body Width:

Between 0.342 inches and 0.358 inches

## **Body Height:**

Between 0.064 inches and 0.100 inches

## **Maximum Power Dissipation Rating:**

159.0 milliwatts

#### **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

## Storage Tempurature Range:

-65.0/+150.0 degrees celsius

#### **Features Provided:**

Hermetically sealed and burn in and schottky and monolithic and positive outputs

#### Inclosure Material:

Ceramic

### Inclosure Configuration:

Leadless flat pack

### **Output Logic Form:**

Transistor-transistor logic

## Input Circuit Pattern:

Triple 3 input

#### **Design Function And Quantity:**

3 gate, and

## Case Outline Source And Designator:

C-2 mil-m-38510

## **Terminal Surface Treatment:**

Solder

## Voltage Rating And Type Per Characteristic:

-0.5 volts power source and 7.0 volts power source

## **Time Rating Per Chacteristic:**

7.50 nanoseconds propagation delay time, low to high level output and 7.50 nanoseconds propagation delay time, high to low level output

#### **Test Data Document:**

81349-mil-m-38510 specification (includes engineering type bulletins, brochures, etc., that reflect specification type data in specification format; excludes commercial catalogs, industry directories, and similar trade publications, reflecting general type data on certain environmental and performance requirements and test conditions that are shown as "typical", "average", "", etc.).

## Terminal Type And Quantity:

20 leadless

## **Specification Data:**

81349-mil-m-38510/340 government specification

## Shelf Life:

N/a

Unit Of Measure:

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## Demilitarization:

Yes - demil/mli

Fiig:

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