NSN 5962-01-286-1605

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Body Length:
Between 1.435 inches and 1.485 inches
Body Width:
Between 0.515 inches and 0.600 inches
Body Height:
Between 0.140 inches and 0.187 inches
Operating Tempurature Range:
+0.0/+70.0 degrees celsius
Storage Tempurature Range:
-65.0/+125.0 degrees celsius
Features Provided:
W/enable and monolithic and electrostatic sensitive and programmable and ultraviolet erasable
Inclosure Material:
Ceramic
Inclosure Configuration:
Dual-in-line Dual-in-line
Output Logic Form:
N-type metal oxide-semiconductor logic
Input Circuit Pattern:
17 input
Terminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
-0.6 volts power source and 7.0 volts power source
Time Rating Per Chacteristic:
250.00 nanoseconds propagation delay time, low to high level output and 250.00 nanoseconds propagation delay time, high to low level
output
Memory Device Type:
Rom
Test Data Document:
96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).
Terminal Type And Quantity:
28 printed circuit
Shelf Life:
N/a
Unit Of Measure:
Demilitarization:
Yes - demil/mli

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