NSN 5962-01-287-3431

Memory Microcircuit - Page 1 of 1



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Body Length:

Between 1.235 inches and 1.285 inches

Body Width:

Between 0.510 inches and 0.550 inches

Body Height:

Between 0.140 inches and 0.180 inches

Operating Tempurature Range:

+0.0/+70.0 degrees celsius

Storage Tempurature Range:

-65.0/+150.0 degrees celsius

End Application:

4920-01-187-4068

Features Provided:

Hermetically sealed and programmable and erasable and w/enable and low power

Inclosure Material:

Ceramic

Inclosure Configuration:

Dual-in-line

Output Logic Form:

Transistor-transistor logic

Input Circuit Pattern:

22 input

Terminal Surface Treatment:

Solder

Voltage Rating And Type Per Characteristic:

-0.3 volts power source and 6.0 volts power source

Time Rating Per Chacteristic:

300.00 nanoseconds propagation delay time, low to high level output and 300.00 nanoseconds propagation delay time, high to low level output

Memory Device Type:

Rom

Memory Capacity:

Unknown

Terminal Type And Quantity:

24 printed circuit

Shelf Life:

N/a

Unit Of Measure:

Demilitarization:

Yes - demil/mli

Fiig:

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