NSN 5962-01-288-3045

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Operating Tempurature Range:
-55.0/+125.0 degrees celsius
Features Provided:
Positive edge triggered and high speed and burn in and w/clock and monolithic
Inclosure Material:
Ceramic
Inclosure Configuration:
Flat pack
Output Logic Form:
Transistor-transistor logic
Input Circuit Pattern:
Dual 5 input
Design Function And Quantity:
2 flip-flop, j-k
Terminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
5.0 volts power source
Time Rating Per Chacteristic:
7.00 nanoseconds propagation delay
Terminal Type And Quantity:
16 flat leads
Shelf Life:
N/a
Unit Of Measure:
Demilitarization:
Yes - demil/mli
Fiia: