NSN 5962-01-291-4128

Digital Microcircuit - Page 1 of 1



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Opera	atina	Tem	ourat	ure	Range:

-55.0/+125.0 degrees celsius

Features Provided:

Asynchronous and compatible cmos and high impedance and high performance and high speed and w/active pull-up and w/common clock and w/reset and 3-state output

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Ceramic

Inclosure Configuration:

Dual-in-line

Output Logic Form:

Complementary-metal oxide-semiconductor logic and n-type metal oxide-semiconductor logic and transistor-transistor logic

Input Circuit Pattern:

Dual 4 wide 4 input

Case Outline Source And Designator:

7a joint electron device engineering council

Shelf Life:

N/a

Unit Of Measure:

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Demilitarization:

Yes - demil/mli

Fiig:

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