NSN 5962-01-291-4129

Digital Microcircuit - Page 1 of 1



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Operating Tempurature Range:

-55.0/+125.0 degrees celsius

Features Provided:

W/enable and high performance and compatible cmos

Inclosure Material:

Ceramic

Inclosure Configuration:

Dual-in-line

Output Logic Form:

Complementary-metal oxide-semiconductor logic and n-type metal oxide-semiconductor logic and transistor-transistor logic

Input Circuit Pattern:

Quad 2 input

Design Function And Quantity:

2 buffer, noninverting and 2 driver, clock and 2 gate

Case Outline Source And Designator:

7a joint electron device engineering council

Shelf Life:

N/a

Unit Of Measure:

Demilitarization:

Yes - demil/mli

Fiig:

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