## NSN 5962-01-291-4131

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Operating Tempurature Range:
-55.0/+125.0 degrees celsius
Features Provided:
Compatible cmos and high performance and high speed and cascadable
Inclosure Material:
Ceramic
Inclosure Configuration:
Dual-in-line
Output Logic Form:
Complementary-metal oxide-semiconductor logic and n-type metal oxide-semiconductor logic and transistor-transistor logic
Input Circuit Pattern:
4 wide 2 input
Design Function And Quantity:
2 decoder, one of eight line and 2 latch, addressable
Case Outline Source And Designator:
7a joint electron device engineering council
Shelf Life:
N/a
Unit Of Measure:
<del></del>
Demilitarization:
Yes - demil/mli
Fiig:

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