## NSN 5962-01-292-2111

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| Body Length:   |
|--|
| 0.640 inches   |
| Body Width:  |
| Between 0.360 inches and 0.420 inches  |
| Body Height:   |
| Between 0.045 inches and 0.090 inches  |
| Maximum Power Dissipation Rating:  |
| 630.0 milliwatts   |
| Operating Tempurature Range:   |
| -55.0/+125.0 degrees celsius   |
| Storage Tempurature Range:   |
| -65.0/+150.0 degrees celsius   |
| Features Provided:   |
| W/clear and hermetically sealed and w/enable   |
| Inclosure Material:  |
| Ceramic  |
| Inclosure Configuration:   |
| Flat pack  |
| Output Logic Form:   |
| Transistor-transistor logic  |
| Input Circuit Pattern:   |
| Dual 7 input   |
| Design Function And Quantity:  |
| 2 latch  |
| Case Outline Source And Designator:  |
| F-6 mil-m-38510  |
| Terminal Surface Treatment:  |
| Solder   |
| Voltage Rating And Type Per Characteristic:  |
| -0.5 volts power source and 7.0 volts power source   |
| Test Data Document:  |
| 96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.). |
| Terminal Type And Quantity:  |
| 24 flat leads  |
| Shelf Life:  |
| N/a  |
| Unit Of Measure:   |
| <del></del>  |
| Demilitarization:  |
| Yes - demil/mli  |
| Fiig:  |
| A458a0   |