## NSN 5962-01-292-8295

Memory Microcircuit - Page 1 of 1



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Body Length:
Between 1.170 inches and 1.285 inches
Body Width:
Between 0.245 inches and 0.285 inches
Body Height:
0.205 inches
Operating Tempurature Range:
-55.0/+125.0 degrees celsius
Storage Tempurature Range:
-65.0/+150.0 degrees celsius
Features Provided:
Programmable and bipolar and electrostatic sensitive and w/enable and edge triggered
Inclosure Material:
Ceramic
Inclosure Configuration:
Dual-in-line
Output Logic Form:
Transistor-transistor logic
Input Circuit Pattern:
16 input
Terminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
7.0 volts power source
Time Rating Per Chacteristic:
30.00 nanoseconds propagation delay time, low to high level output and 35.00 nanoseconds propagation delay time, high to low level
output
Memory Device Type:
Rom
Test Data Document:
96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).
Terminal Type And Quantity:
24 printed circuit
Shelf Life:
N/a
Unit Of Measure:
<del></del>
Demilitarization:
Yes - demil/mli
Fiig:
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