NSN 5962-01-293-0658

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Operating Tempurature Range:
-55.0/+125.0 degrees celsius
Features Provided:
Asynchronous and cascadable and compatible cmos and high performance and high speed and w/decoded output and w/active pull-up and
w/resistor
Inclosure Material:
Ceramic
Inclosure Configuration:
Dual-in-line
Output Logic Form:
Complementary-metal oxide-semiconductor logic and n-type metal oxide-semiconductor logic and transistor-transistor logic
Input Circuit Pattern:
Dual 4 stage
Design Function And Quantity:
1 counter, binary
Case Outline Source And Designator:
7a joint electron device engineering council
Voltage Rating And Type Per Characteristic:
7.0 volts power source
Terminal Type And Quantity:
14 printed circuit
Shelf Life:
N/a
Unit Of Measure:
Demilitarization:
No
Fiig:
A458a0