# NSN 5962-01-293-0659

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# **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

### **Features Provided:**

W/clock and w/reset and high performance

#### **Inclosure Material:**

Ceramic

# Inclosure Configuration:

Dual-in-line

#### **Output Logic Form:**

Complementary-metal oxide-semiconductor logic and n-type metal oxide-semiconductor logic and transistor-transistor logic

#### Input Circuit Pattern:

Dual 3 input

**Design Function And Quantity:** 

2 flip-flop, j-k and 2 clock

#### **Case Outline Source And Designator:**

7a joint electron device engineering council

#### Shelf Life:

N/a

#### Unit Of Measure:

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Demilitarization:

Yes - demil/mli

# Fiig:

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