NSN 5962-01-294-6723

Memory Microcircuit - Page 1 of 1



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| Operating Tempurature Range: |
|--|
| -55.0/+125.0 degrees celsius |
| Storage Tempurature Range: |
| -65.0/+150.0 degrees celsius |
| Features Provided: |
| Programmed and 3-state output and high impedance and w/enable and monolithic |
| Inclosure Material: |
| Ceramic or plastic |
| Inclosure Configuration: |
| Dual-in-line |
| Output Logic Form: |
| Transistor-transistor logic |
| Input Circuit Pattern: |
| 14 input |
| Current Rating Per Characteristic: |
| 185.00 milliamperes reverse current, dc absolute |
| Terminal Surface Treatment: |
| Solder |
| Voltage Rating And Type Per Characteristic: |
| -0.3 volts power source and 7.0 volts power source |
| Time Rating Per Chacteristic: |
| 100.00 nanoseconds access |
| Memory Device Type: |
| Prom |
| Test Data Document: |
| $96906 \hbox{-mil-std-}883 \ standard \ (includes \ industry \ or \ association \ standards, \ individual \ manufactureer \ standards, \ etc.).$ |
| Terminal Type And Quantity: |
| 24 printed circuit |
| Shelf Life: |
| N/a |
| Unit Of Measure: |
| |
| Demilitarization: |
| Yes - demil/mli |
| Fiig: |
| A458a0 |
| |