NSN 5962-01-301-3137

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Operating Tempurature Range:
-55.0/+125.0 degrees celsius
Inclosure Material:
Ceramic
Inclosure Configuration:
Pin grid array
Output Logic Form:
Emitter-coupled logic and transistor-transistor logic
Design Function And Quantity:
1 gate, array
Terminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
-8.0 volts power source and 7.0 volts power source
Test Data Document:
96906 mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).
Terminal Type And Quantity:
148 pin
Shelf Life:
N/a
Unit Of Measure:
Demilitarization:
Yes - demil/mli
Fiig:
A458a0