# NSN 5962-01-301-7012

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# **Body Length:**

Between 0.990 inches and 0.995 inches

# Body Width:

Between 0.265 inches and 0.300 inches

# **Body Height:**

0.190 inches

## **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

## Storage Tempurature Range:

-65.0/+150.0 degrees celsius

## **End Application:**

5940-01-238-4021 terminal, taper

## **Features Provided:**

Hermetically sealed and burn in and programmable and positive outputs

#### Inclosure Material:

Ceramic

## Inclosure Configuration:

Dual-in-line

## **Output Logic Form:**

Transistor-transistor logic

## Input Circuit Pattern:

18 input

## **Design Function And Quantity:**

1 array, logic

# **Terminal Surface Treatment:**

Solder

## Voltage Rating And Type Per Characteristic:

-0.5 volts power source and 7.0 volts power source

## Time Rating Per Chacteristic:

45.00 nanoseconds propagation delay time, low to high level output and 45.00 nanoseconds propagation delay time, high to low level output

## **Test Data Document:**

96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).

## Terminal Type And Quantity:

20 printed circuit

#### Shelf Life:

N/a

#### Unit Of Measure:

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#### Demilitarization:

Yes - demil/mli

#### Fiig:

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