NSN 5962-01-304-5990

Memory Microcircuit - Page 1 of 1



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Body Length: 1.490 inches Body Width: 0.610 inches Body Height: 0.217 inches Maximum Power Dissipation Rating:
Body Width: 0.610 inches Body Height: 0.217 inches
0.610 inches Body Height: 0.217 inches
Body Height: 0.217 inches
0.217 inches
Maximum Power Dissinction Pating:
1.0 watts
Operating Tempurature Range:
-55.0/+125.0 degrees celsius
Storage Tempurature Range:
-65.0/+150.0 degrees celsius
Features Provided:
Monolithic and ultraviolet erasable and programmable
Inclosure Material:
Ceramic
Inclosure Configuration:
Dual-in-line
Output Logic Form:
Transistor-transistor logic
Input Circuit Pattern:
16 input
Case Outline Source And Designator:
D-10 mil-m-38510
Terminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
7.0 volts power source
Memory Device Type:
Rom
Test Data Document:
96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).
Terminal Type And Quantity:
28 printed circuit
Shelf Life:
N/a
Unit Of Measure:
Demilitarization:
Yes - demil/mli
Fiig:
A458a0