## NSN 5962-01-310-5453

Digital Microcircuit - Page 1 of 2



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**Overall Height:** 

Between 0.325 inches and 0.400 inches

**Body Length:** 

1.060 inches

**Body Width:** 

Between 0.220 inches and 0.310 inches

**Body Height:** 

Between 0.130 inches and 0.185 inches

**Maximum Power Dissipation Rating:** 

1.1 watts

**Operating Tempurature Range:** 

-55.0/+125.0 degrees celsius

**Storage Tempurature Range:** 

-65.0/+150.0 degrees celsius

**Features Provided:** 

Hermetically sealed and burn in and programmable and bipolar and monolithic

**Inclosure Material:** 

Ceramic

**Inclosure Configuration:** 

Dual-in-line

**Output Logic Form:** 

Transistor-transistor logic

**Input Circuit Pattern:** 

16 input

**Design Function And Quantity:** 

1 gate, array

**Case Outline Source And Designator:** 

D-8 mil-m-38510

**Current Rating Per Characteristic:** 

200.00 microamperes input

**Terminal Surface Treatment:** 

Solder

**Voltage Rating And Type Per Characteristic:** 

-0.5 volts power source and 10.7 volts power source

**Time Rating Per Chacteristic:** 

20.00 nanoseconds propagation delay time, low to high level output and 20.00 nanoseconds propagation delay time, high to low level output

**Test Data Document:** 

81349-mil-m-38510 specification (includes engineering type bulletins, brochures, etc., that reflect specification type data in specification format; excludes commercial catalogs, industry directories, and similar trade publications, reflecting general type data on certain environmental and performance requirements and test conditions that are shown as "typical", "average", "", etc.).

## **Terminal Type And Quantity:**

20 printed circuit

**Specification Data:** 

813/0-mil-m-38510/506 government specification

## NSN 5962-01-310-5453

Digital Microcircuit - Page 2 of 2



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N/a

**Unit Of Measure:** 

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Demilitarization:

No

Fiig:

A458a0