NSN 5962-01-311-3258

Memory Microcircuit - Page 1 of 1



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Body Length:
1.375 inches
Body Width:
0.500 inches
Body Height:
0.185 inches
Maximum Power Dissipation Rating:
825.0 milliwatts
Operating Tempurature Range:
-55.0/+125.0 degrees celsius
Storage Tempurature Range:
-65.0/+150.0 degrees celsius
Features Provided:
Burn in and monolithic and programmed and ultraviolet erasable
Inclosure Material:
Ceramic
Inclosure Configuration:
Dual-in-line
Output Logic Form:
Transistor-transistor logic
Input Circuit Pattern:
13 input
Case Outline Source And Designator:
D-10 mil-m-38510
Terminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
-0.6 volts power source and 14.0 volts power source
Memory Device Type:
Rom
Test Data Document:
96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.)
Terminal Type And Quantity:
28 printed circuit
Shelf Life:
N/a
Unit Of Measure:
Demilitarization:
Yes - demil/mli
Fiig:
A458a0