NSN 5962-01-314-2046

Digital Microcircuit - Page 1 of 1



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Features Provided:
Electrostatic sensitive and programmed
Inclosure Configuration:
Dual-in-line
Output Logic Form:
Transistor-transistor logic
Criticality Code Justification:
Feat
Special Features:
Esd
Shelf Life:
N/a
Unit Of Measure:
Demilitarization:
No
Fiig:
A458a0