## NSN 5962-01-314-9195

Digital Microcircuit - Page 1 of 1



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| Body Length:   |
|--|
| 0.358 inches   |
| Body Width:  |
| 0.358 inches   |
| Body Height:   |
| 0.100 inches   |
| Operating Tempurature Range:   |
| -55.0/+125.0 degrees celsius   |
| Storage Tempurature Range:   |
| -65.0/+150.0 degrees celsius   |
| Features Provided:   |
| Monolithic   |
| Inclosure Material:  |
| Ceramic  |
| Inclosure Configuration:   |
| Leadless flat pack   |
| Output Logic Form:   |
| Transistor-transistor logic  |
| Input Circuit Pattern:   |
| Octal 2 input  |
| Design Function And Quantity:  |
| 8 transceiver  |
| Case Outline Source And Designator:  |
| C-2 mil-m-38510  |
| Terminal Surface Treatment:  |
| Solder   |
| Voltage Rating And Type Per Characteristic:  |
| 7.0 volts power source   |
| Time Rating Per Chacteristic:  |
| 18.00 nanoseconds propagation delay time, low to high level output and 18.00 nanoseconds propagation delay time, high to low level |
| output   |
| Test Data Document:  |
| 96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).                 |
| Terminal Type And Quantity:  |
| 20 leadless  |
| Shelf Life:  |
| N/a  |
| Unit Of Measure:   |
| <del></del>  |
| Demilitarization:  |
| Yes - demil/mli  |

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