## NSN 5962-01-315-6309

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Operating Tempurature Range:
+0.0/+70.0 degrees celsius
Features Provided:
High speed and bipolar and w/inhibit and monolithic
Inclosure Material:
Ceramic
Inclosure Configuration:
Dual-in-line
Output Logic Form:
Transistor-transistor logic
Input Circuit Pattern:
7 input
Design Function And Quantity:
2 driver
Terminal Surface Treatment:
Solder
Time Rating Per Chacteristic:
25.00 nanoseconds propagation delay time, low to high level output and 25.00 nanoseconds propagation delay time, high to low level
output
Terminal Type And Quantity:
14 printed circuit
Shelf Life:
N/a
Unit Of Measure:
<del></del>
Demilitarization:
Yes - demil/mli
Fiig:
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