NSN 5962-01-316-3514

Memory Microcircuit - Page 1 of 1



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Overall Height:
Between 0.215 inches and 0.350 inches
Body Length:
Between 0.750 inches and 0.830 inches
Body Width:
Between 0.240 inches and 0.310 inches
Body Height:
Between 0.030 inches and 0.185 inches
Operating Tempurature Range:
-55.0/+125.0 degrees celsius
Storage Tempurature Range:
-65.0/+150.0 degrees celsius
End Application:
Training set, countermeasures signals an/msr-t4 tra
Features Provided:
Low power and w/enable and asynchronous and expandable and high performance
Inclosure Material:
Ceramic
Inclosure Configuration:
Dual-in-line
Output Logic Form:
Complementary-metal oxide-semiconductor logic
Input Circuit Pattern:
8 input
Terminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
-0.5 volts power source and 7.0 volts power source
Memory Device Type:
Rom
Test Data Document:
96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).
Terminal Type And Quantity:
16 printed circuit
Shelf Life:
N/a
Unit Of Measure:

Demilitarization:
Yes - demil/mli

Fiig:

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