NSN 5962-01-316-5954

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Between 0.342 inches and 0.358 inches

Body Width:

Between 0.342 inches and 0.358 inches

Body Height:

Between 0.060 inches and 0.100 inches

Maximum Power Dissipation Rating:

149.0 milliwatts

Operating Tempurature Range:

-55.0/+125.0 degrees celsius

Storage Tempurature Range:

-65.0/+150.0 degrees celsius

Features Provided:

Cascadable and monolithic and w/clear and schottky and low power and bipolar

Inclosure Material:

Ceramic

Inclosure Configuration:

Leadless flat pack

Output Logic Form:

Transistor-transistor logic

Input Circuit Pattern:

10 input

Design Function And Quantity:

8 flip-flop, d-type

Case Outline Source And Designator:

C-2 mil-m-38510

Terminal Surface Treatment:

Solder

Voltage Rating And Type Per Characteristic:

7.0 volts power source

Time Rating Per Chacteristic:

42.00 nanoseconds propagation delay time, low to high level output and 42.00 nanoseconds propagation delay time, high to low level output

Test Data Document:

81349-mil-m-38510 specification (includes engineering type bulletins, brochures, etc., that reflect specification type data in specification format; excludes commercial catalogs, industry directories, and similar trade publications, reflecting general type data on certain environmental and performance requirements and test conditions that are shown as "typical", "average", "", etc.).

Terminal Type And Quantity:

20 leadless

Specification Data:

81349-mil-m-38510/325 government specification

Shelf Life:

N/a

Unit Of Measure:

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Demilitarization:

Yes - demil/mli

Fiig:

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