NSN 5962-01-317-4140

Memory Microcircuit - Page 1 of 1



individual manufactureer standards, etc.).

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Body Le	ngth:
0.925 inc	hes
Body Wi	dth:
0.302 inc	hes
Body He	ight:
0.175 inc	hes
Maximu	n Power Dissipation Rating:
409.6 mil	liwatts
Operatin	g Tempurature Range:
-55.0/+12	25.0 degrees celsius
Storage	Tempurature Range:
-65.0/+18	50.0 degrees celsius
Features	S Provided:
Monolithi	c and bipolar and 3-state output
Inclosur	e Material:
Ceramic	
Inclosur	e Configuration:
Dual-in-li	ne
Output L	.ogic Form:
Transisto	or-transistor logic
Input Cir	cuit Pattern:
8 input	
Termina	I Surface Treatment:
Solder	
Voltage	Rating And Type Per Characteristic:
7.0 volts	power source
Memory	Device Type:
Rom	
Test Dat	a Document:
96906-m	il-std-883 standard (includes industry or association standards
Termina	I Type And Quantity:
18 printe	d circuit
Shelf Lif	e:
N/a	
Unit Of N	Measure:
Demilita	rization:
Yes - der	nil/mli
Fiig:	
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