## NSN 5962-01-320-7875



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## Memory Microcircuit - Page 1 of 2 **Overall Height:** 0.425 inches **Body Length:** 1.290 inches **Body Width:** Between 0.500 inches and 0.610 inches **Body Height:** Between 0.150 inches and 0.210 inches **Maximum Power Dissipation Rating:** 1.04 watts **Operating Tempurature Range:** -55.0/+125.0 degrees celsius **Storage Tempurature Range:** -65.0/+150.0 degrees celsius **Features Provided:** Bipolar and programmable and schottky and monolithic and 3-state output **Inclosure Material:** Ceramic Inclosure Configuration: Dual-in-line **Output Logic Form:** Transistor-transistor logic **Input Circuit Pattern:** 14 input **Case Outline Source And Designator:** D-3 mil-m-38510 **Terminal Surface Treatment:** Solder Voltage Rating And Type Per Characteristic: -0.3 volts power source and 7.0 volts power source Memory Device Type: Rom **Test Data Document:** 96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.). **Terminal Type And Quantity:** 24 printed circuit Shelf Life: N/a Unit Of Measure: ---**Demilitarization:**

Yes - demil/mli

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**Fiig:** A458a0

