## NSN 5962-01-323-4960

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## **Body Length:** 1.060 inches **Body Width:** Between 0.220 inches and 0.310 inches **Body Height:** Between 0.130 inches and 0.185 inches **Maximum Power Dissipation Rating:** 275.0 milliwatts **Operating Tempurature Range:** -55.0/+125.0 degrees celsius Storage Tempurature Range: -65.0/+150.0 degrees celsius **Features Provided:** Hermetically sealed and burn in and low power and schottky and bipolar **Inclosure Material:** Ceramic **Inclosure Configuration:** Dual-in-line **Output Logic Form:** Transistor-transistor logic **Input Circuit Pattern:** 10 input **Design Function And Quantity:** 8 gate, bus buffer, inverting **Case Outline Source And Designator:** D-8 mil-m-38510 **Terminal Surface Treatment:** Solder Voltage Rating And Type Per Characteristic: -0.5 volts power source and 7.0 volts power source **Time Rating Per Chacteristic:** 18.00 nanoseconds propagation delay time, low to high level output and 23.00 nanoseconds propagation delay time, high to low level output **Test Data Document:** 96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.). **Terminal Type And Quantity:** 20 printed circuit Shelf Life: N/a Unit Of Measure: **Demilitarization:** Yes - demil/mli

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