## NSN 5962-01-324-9850

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Operating Tempurature Range:
-55.0/+125.0 degrees celsius
Features Provided:
Hermetically sealed and monolithic and positive outputs and w/open collector
Inclosure Material:
Ceramic and glass
Inclosure Configuration:
Dual-in-line
Output Logic Form:
Transistor-transistor logic
Input Circuit Pattern:
Quad 2 input
Case Outline Source And Designator:
D-1 mil-m-38510
Test Data Document:
81349-mil-m-38510 specification (includes engineering type bulletins, brochures, etc., that reflect specification type data in specification
format; excludes commercial catalogs, industry directories, and similar trade publications, reflecting general type data on certain
environmental and performance requirements and test conditions that are shown as "typical", "average", "", etc.).
Specification Data:
81349-mil-m-38510/3 government specification
Shelf Life:
N/a
Unit Of Measure:
Demilitarization:
Yes - demil/mli
Fiig: