# NSN 5962-01-333-1646

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## **Body Length:**

Between 1.088 inches and 1.150 inches

## **Body Width:**

Between 1.088 inches and 1.150 inches

## **Body Height:**

Between 0.077 inches and 0.095 inches

#### **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

#### Storage Tempurature Range:

-65.0/+150.0 degrees celsius

#### **Features Provided:**

Monolithic and electrostatic sensitive

#### **Inclosure Material:**

Ceramic

#### Inclosure Configuration:

Quad-in-line

## **Output Logic Form:**

Transistor-transistor logic

## Input Circuit Pattern:

41 input

## Criticality Code Justification:

Feat

#### **Design Function And Quantity:**

1 gate, array

#### **Terminal Surface Treatment:**

Solder

# Voltage Rating And Type Per Characteristic:

5.5 volts power source

# **Special Features:**

Hardness critical process

# Test Data Document:

96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).

# Terminal Type And Quantity:

68 pin

# Shelf Life:

N/a

# Unit Of Measure:

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## Demilitarization:

No

Fiig:

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