NSN 5962-01-333-1647

Digital Microcircuit - Page 1 of 1



manufactureer standards, etc.).

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Body Ler	igth:	
0.840 incł	nes	
Body Wid	ith:	
0.310 incł	nes	
Body Hei	ght:	
0.200 incł	nes	
Maximun	Power Dissipation Rating:	
25.0 milliv	vatts	
Operating	g Tempurature Range:	
-55.0/+12	5.0 degrees celsius	
Storage 1	Fempurature Range:	
-65.0/+15	0.0 degrees celsius	
Features	Provided:	
Monolithic	and w/clear and w/clock	
Inclosure	Material:	
Ceramic		
Inclosure	Configuration:	
Dual-in-lir	e	
Output Lo	ogic Form:	
Transistor	-transistor logic	
Input Cire	cuit Pattern:	
6 input		
Design F	unction And Quantity:	
4 flip-flop,	d-type	
Case Out	line Source And Designator:	
D-2 mil-m	-38510	
Terminal	Surface Treatment:	
Solder		
Voltage F	ating And Type Per Characteristic:	
7.0 volts	power source	
Test Data	Document:	
96906-mil	-std-883 standard (includes industry or association standards,	individua
Terminal	Type And Quantity:	
16 printed	circuit	
Shelf Life	::	
N/a		
Unit Of M	easure:	
Demilitar	ization:	
Yes - dem	il/mli	
Fiig:		
A458a0		