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Operating Temperature Range:

-55.0/+125.0 degrees celsius

Inclosure Configuration:

Flat pack

Output Logic Form:

Transistor-transistor logic

Design Function And Quantity:

2 gate, and-or invert

Case Outline Source And Designator:

F-2 mil-m-38510

Shelf Life:

N/a

Unit Of Measure:

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Demilitarization:

Yes - demil/mli

Fig:

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