## NSN 5962-01-335-3523

Memory Microcircuit - Page 1 of 1

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## **Overall Height:** 0.357 inches **Body Length:** Between 1.235 inches and 1.285 inches **Body Width:** Between 0.515 inches and 0.600 inches **Body Height:** Between 0.145 inches and 0.187 inches **Maximum Power Dissipation Rating:** 165.0 milliwatts **Operating Tempurature Range:** -55.0/+100.0 degrees celsius **Storage Tempurature Range:** -65.0/+125.0 degrees celsius **Features Provided:** Hermetically sealed and ultraviolet erasable and programmable and monolithic **Inclosure Material:** Ceramic Inclosure Configuration: Dual-in-line **Output Logic Form:** Transistor-transistor logic **Input Circuit Pattern:** 14 input **Terminal Surface Treatment:** Solder Voltage Rating And Type Per Characteristic: 6.0 volts power source Memory Device Type: Rom **Terminal Type And Quantity:** 24 printed circuit Shelf Life: N/a Unit Of Measure: --Demilitarization: Yes - demil/mli Fiig: A458a0