## NSN 5962-01-339-1701

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Between 0.325 inches and 0.400 inches

**Body Length:** 

1.060 inches

**Body Width:** 

Between 0.220 inches and 0.310 inches

**Body Height:** 

Between 0.130 inches and 0.185 inches

**Maximum Power Dissipation Rating:** 

2.0 watts

**Operating Tempurature Range:** 

-55.0/+125.0 degrees celsius

Storage Tempurature Range:

-65.0/+150.0 degrees celsius

**Features Provided:** 

Bipolar and monolithic and hermetically sealed and burn in

**Inclosure Material:** 

Ceramic

**Inclosure Configuration:** 

Dual-in-line

**Output Logic Form:** 

Transistor-transistor logic

**Input Circuit Pattern:** 

10 input

**Case Outline Source And Designator:** 

D-8 mil-m-38510

**Current Rating Per Characteristic:** 

180.00 milliamperes supply

**Terminal Surface Treatment:** 

Solder

**Product Name:** 

Registered and-or gate array

**Voltage Rating And Type Per Characteristic:** 

-500.0 millivolts power source and 12.0 volts power source

**Time Rating Per Chacteristic:** 

30.00 nanoseconds propagation delay time, high to low level output and 30.00 nanoseconds propagation delay time, low to high level output

**Memory Device Type:** 

Programmed

**Test Data Document:** 

96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).

**Terminal Type And Quantity:** 

20 printed circuit

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Shelf Life:

N/a

**Unit Of Measure:** 

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Demilitarization:

Yes - demil/mli

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