NSN 5962-01-339-3153

Memory Microcircuit - Page 1 of 1



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Features Provided:
Bidirectional and w/clock and programmable and positive outputs and electrostatic sensitive
Inclosure Material:
Ceramic
Inclosure Configuration:
Leaded chip carrier
Output Logic Form:
Complementary-metal oxide-semiconductor logic
Input Circuit Pattern:
38 input
Terminal Surface Treatment:
Solder
Terminal Type And Quantity:
44 j-hook
Shelf Life:
N/a
Unit Of Measure:
Demilitarization:
Yes - demil/mli
Fiig:
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