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Output Logic Form:

Complementary-metal oxide-semiconductor logic

Input Circuit Pattern:

8 input

Design Function And Quantity:

1 buffer

Supplementary Features:

27sep00 item reviewed under girder project. Zg non-concur cancellation action

Shelf Life:

N/a

Unit Of Measure:

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Demilitarization:

Yes - demil/mli

Fiig:

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