# NSN 5962-01-341-7080

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## **Body Length:**

Between 0.342 inches and 0.358 inches

## Body Width:

Between 0.342 inches and 0.358 inches

## **Body Height:**

Between 0.060 inches and 0.100 inches

## Maximum Power Dissipation Rating:

330.0 milliwatts

#### **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

#### Storage Tempurature Range:

-65.0/+150.0 degrees celsius

#### **Features Provided:**

Bipolar and burn in and cascadable and monolithic and schottky and 3-state output

#### Inclosure Material:

Ceramic

### Inclosure Configuration:

Leadless flat pack

## **Output Logic Form:**

Transistor-transistor logic

## Input Circuit Pattern:

9 input

#### **Design Function And Quantity:**

8 latch

## Case Outline Source And Designator:

C-2 mil-m-38510

## **Terminal Surface Treatment:**

Solder

#### **Product Name:**

Transparent latch with active low enable

## Voltage Rating And Type Per Characteristic:

0.5 volts power source and 7.0 volts power source

#### **Test Data Document:**

81349-mil-m-38510 specification (includes engineering type bulletins, brochures, etc., that reflect specification type data in specification format; excludes commercial catalogs, industry directories, and similar trade publications, reflecting general type data on certain environmental and performance requirements and test conditions that are shown as "typical", "average", "", etc.).

## **Terminal Type And Quantity:**

20 leadless

## **Specification Data:**

81349-mil-m-38510/346 government specification

#### Shelf Life:

N/a

Unit Of Measure:

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## Demilitarization:

Yes - demil/mli

Fiig:

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