## NSN 5962-01-341-7081

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| Maximum Power Dissipation Rating:  |
|--|
| 1.0 watts  |
| Operating Tempurature Range:   |
| -65.0/+150.0 degrees celsius   |
| Storage Tempurature Range:   |
| -55.0/+150.0 degrees celsius   |
| Features Provided:   |
| Monolithic   |
| Inclosure Material:  |
| Ceramic  |
| Inclosure Configuration:   |
| Dual-in-line   |
| Output Logic Form:   |
| Transistor-transistor logic  |
| Input Circuit Pattern:   |
| 27 input   |
| Criticality Code Justification:  |
| Zzzy   |
| Design Function And Quantity:  |
| 1 register, arithmetic   |
| Terminal Surface Treatment:  |
| Solder   |
| Voltage Rating And Type Per Characteristic:  |
| 8.0 volts input  |
| Special Features:  |
| Item must comply with requirements of defense supply center columbus production standard no. Lo4398  |
| Test Data Document:  |
| 96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).                             |
| Terminal Type And Quantity:  |
| 40 printed circuit   |
| Reference Number Differentiating Characteristics:  |
| Breference number differentiating material will be in accordance with naval inventory control point activity hx quality control. Manufacturing |
| and testing specifications available at the dla icp.   |
| Shelf Life:  |
| N/a  |
| Unit Of Measure:   |
| <del></del>  |
| Demilitarization:  |
| Yes - demil/mli  |
| Fiig:  |
|  |