NSN 5962-01-344-9318

Memory Microcircuit - Page 1 of 1



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Storage Tempurature Range:
-65.0/+150.0 degrees celsius
End Application:
1260-01-165-3959 control-display
Features Provided:
Electrostatic sensitive and hermetically sealed and bipolar and programmable
Inclosure Material:
Ceramic
Inclosure Configuration:
Dual-in-line
Output Logic Form:
Transistor-transistor logic
Input Circuit Pattern:
22 input
Design Function And Quantity:
1 array, logic
Current Rating Per Characteristic:
180.00 milliamperes reverse current, dc absolute
Terminal Surface Treatment:
Solder
Product Name:
Array logic, i/o decoder 2
Voltage Rating And Type Per Characteristic:
-0.5 volts power source and 7.0 volts power source
Capitance Rating Per Characteristic:
Err-060 and err-060
Memory Device Type:
Programmed
Unpackaged Unit Weight:
6.5 grams
Terminal Type And Quantity:
24 printed circuit
Shelf Life:
N/a
Unit Of Measure:
Demilitarization:
Yes - demil/mli
Fiig:
A458a0