NSN 5962-01-347-9401

Hybrid Microcircuit - Page 1 of 2



Overall Height: 1.895 inches Overall Height: 0.450 inches Overall Width: 0.775 inches Body Length: 1.895 inches Body Width: 1.895 inches Body Width: 0.775 inches Body Height: 0.210 inches Maximum Power Dissipation Rating: 3.1 watts Operating Tempurature Range: -55.0/+125.0 degrees celsius Storage Tempurature Range: -65.0/+150.0 degrees celsius Features Provided: W/inhibit and w/strobe and monolithic Inclosure Material: Ceramic Inclosure Configuration: Dual-in-line Output Logic Form: Transistor-transistor logic Input Circuit Pattern: 2 channel and 12 input Design Function And Quantity: 1 transceiver Current Rating Per Characteristic: 700.00 milliamperes reverse current, dc absolute Terminal Surface Treatment:	View Online at https://aerobasegroup.com/nsn/5962-01-347-9401
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W/inhibit and w/strobe and monolithic Inclosure Material: Ceramic Inclosure Configuration: Dual-in-line Output Logic Form: Transistor-transistor logic Input Circuit Pattern: 2 channel and 12 input Design Function And Quantity: 1 transceiver Current Rating Per Characteristic: 700.00 milliamperes reverse current, dc absolute	-65.0/+150.0 degrees celsius
Inclosure Material: Ceramic Inclosure Configuration: Dual-in-line Output Logic Form: Transistor-transistor logic Input Circuit Pattern: 2 channel and 12 input Design Function And Quantity: 1 transceiver Current Rating Per Characteristic: 700.00 milliamperes reverse current, dc absolute	Features Provided:
Ceramic Inclosure Configuration: Dual-in-line Output Logic Form: Transistor-transistor logic Input Circuit Pattern: 2 channel and 12 input Design Function And Quantity: 1 transceiver Current Rating Per Characteristic: 700.00 milliamperes reverse current, dc absolute	W/inhibit and w/strobe and monolithic
Inclosure Configuration: Dual-in-line Output Logic Form: Transistor-transistor logic Input Circuit Pattern: 2 channel and 12 input Design Function And Quantity: 1 transceiver Current Rating Per Characteristic: 700.00 milliamperes reverse current, dc absolute	Inclosure Material:
Dual-in-line Output Logic Form: Transistor-transistor logic Input Circuit Pattern: 2 channel and 12 input Design Function And Quantity: 1 transceiver Current Rating Per Characteristic: 700.00 milliamperes reverse current, dc absolute	Ceramic
Output Logic Form: Transistor-transistor logic Input Circuit Pattern: 2 channel and 12 input Design Function And Quantity: 1 transceiver Current Rating Per Characteristic: 700.00 milliamperes reverse current, dc absolute	Inclosure Configuration:
Transistor-transistor logic Input Circuit Pattern: 2 channel and 12 input Design Function And Quantity: 1 transceiver Current Rating Per Characteristic: 700.00 milliamperes reverse current, dc absolute	Dual-in-line
Input Circuit Pattern: 2 channel and 12 input Design Function And Quantity: 1 transceiver Current Rating Per Characteristic: 700.00 milliamperes reverse current, dc absolute	Output Logic Form:
2 channel and 12 input Design Function And Quantity: 1 transceiver Current Rating Per Characteristic: 700.00 milliamperes reverse current, dc absolute	Transistor-transistor logic
Design Function And Quantity: 1 transceiver Current Rating Per Characteristic: 700.00 milliamperes reverse current, dc absolute	Input Circuit Pattern:
1 transceiver Current Rating Per Characteristic: 700.00 milliamperes reverse current, dc absolute	2 channel and 12 input
Current Rating Per Characteristic: 700.00 milliamperes reverse current, dc absolute	Design Function And Quantity:
700.00 milliamperes reverse current, dc absolute	1 transceiver
	Current Rating Per Characteristic:
Terminal Surface Treatment:	700.00 milliamperes reverse current, dc absolute
	Terminal Surface Treatment:

Solder

Product Name:

Dual channel, five volt, mil-std-1553 transceiver

Voltage Rating And Type Per Characteristic:

-0.3 volts power source and 7.0 volts power source

Capitance Rating Per Characteristic:

0.005 input nanofarads and 0.105 output nanofarads

Time Rating Per Chacteristic:

300.00 nanoseconds fall

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Hybrid Microcircuit - Page 2 of 2

Fiig: A458a0



Hybrid Technology Type:
Monolithic
Test Data Document:
96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.
Terminal Type And Quantity:
36 printed circuit
Shelf Life:
N/a
Unit Of Measure:
Demilitarization:
Yes - demil/mli